

FIG. 1

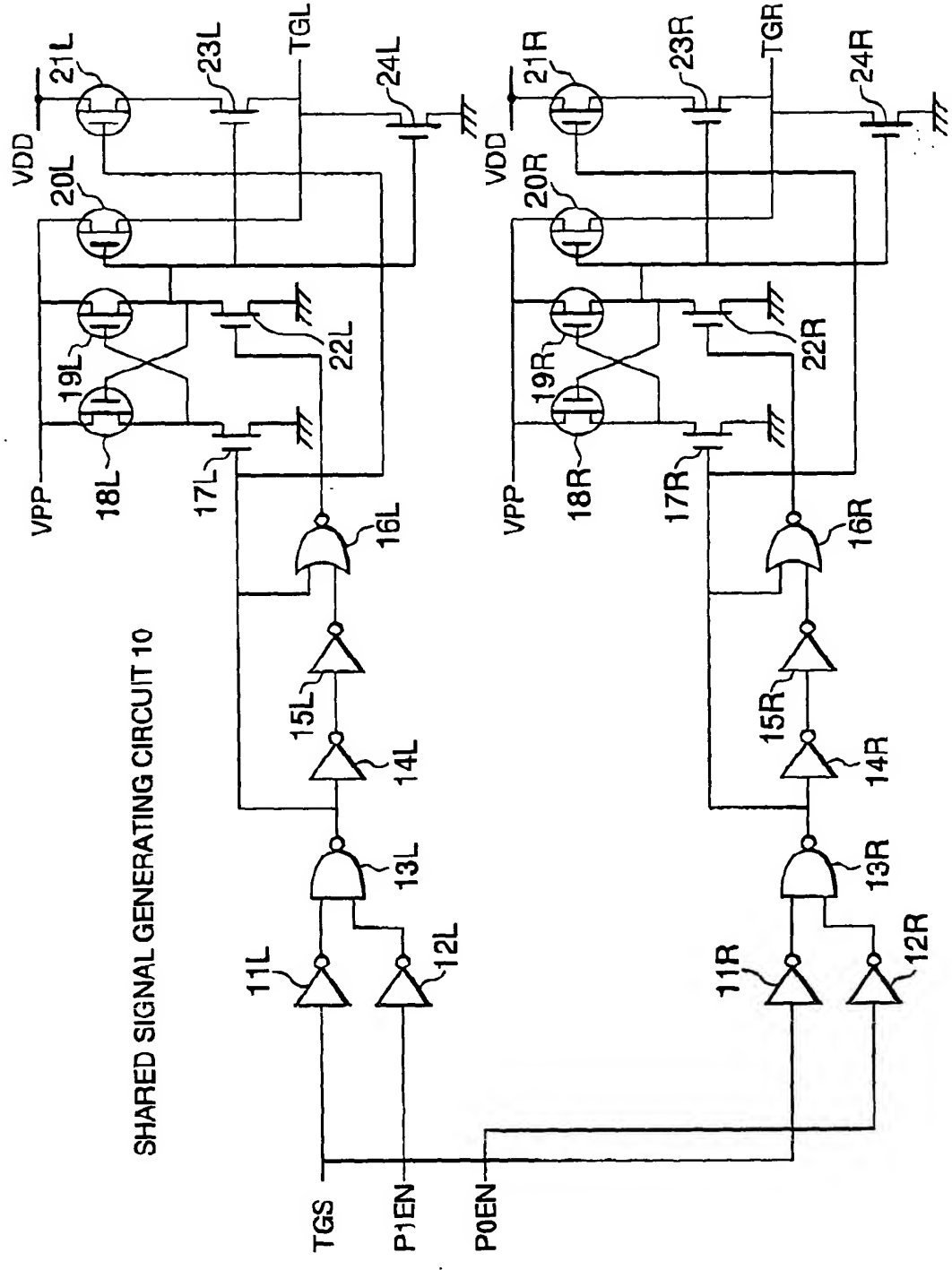


FIG. 2
(PRIOR ART)

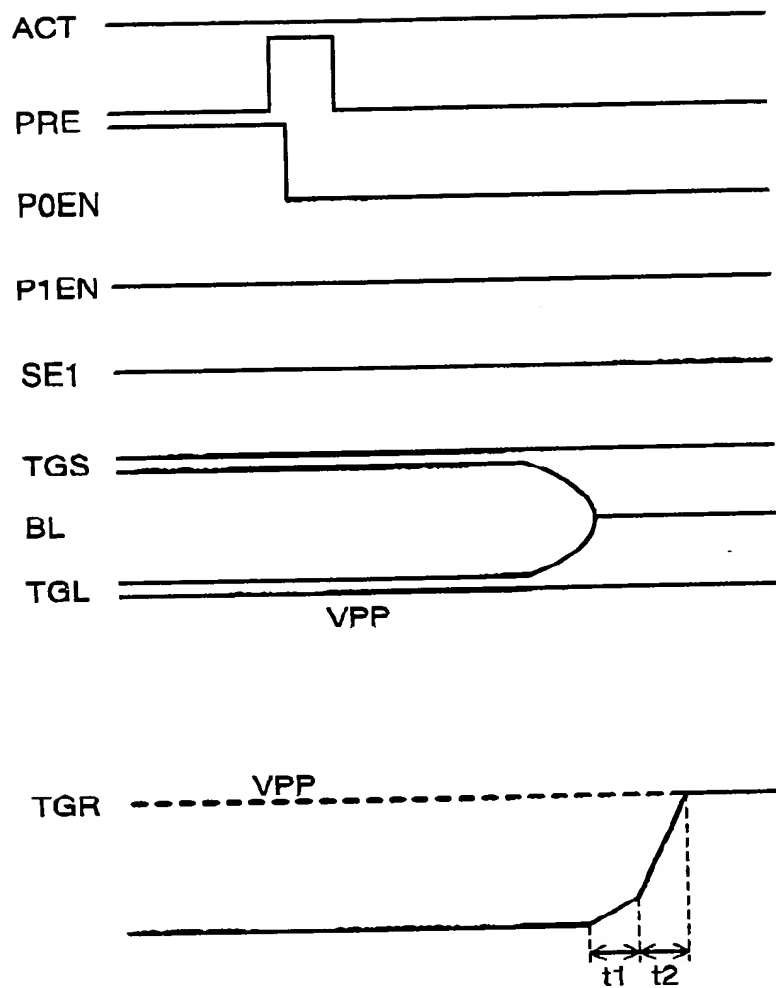
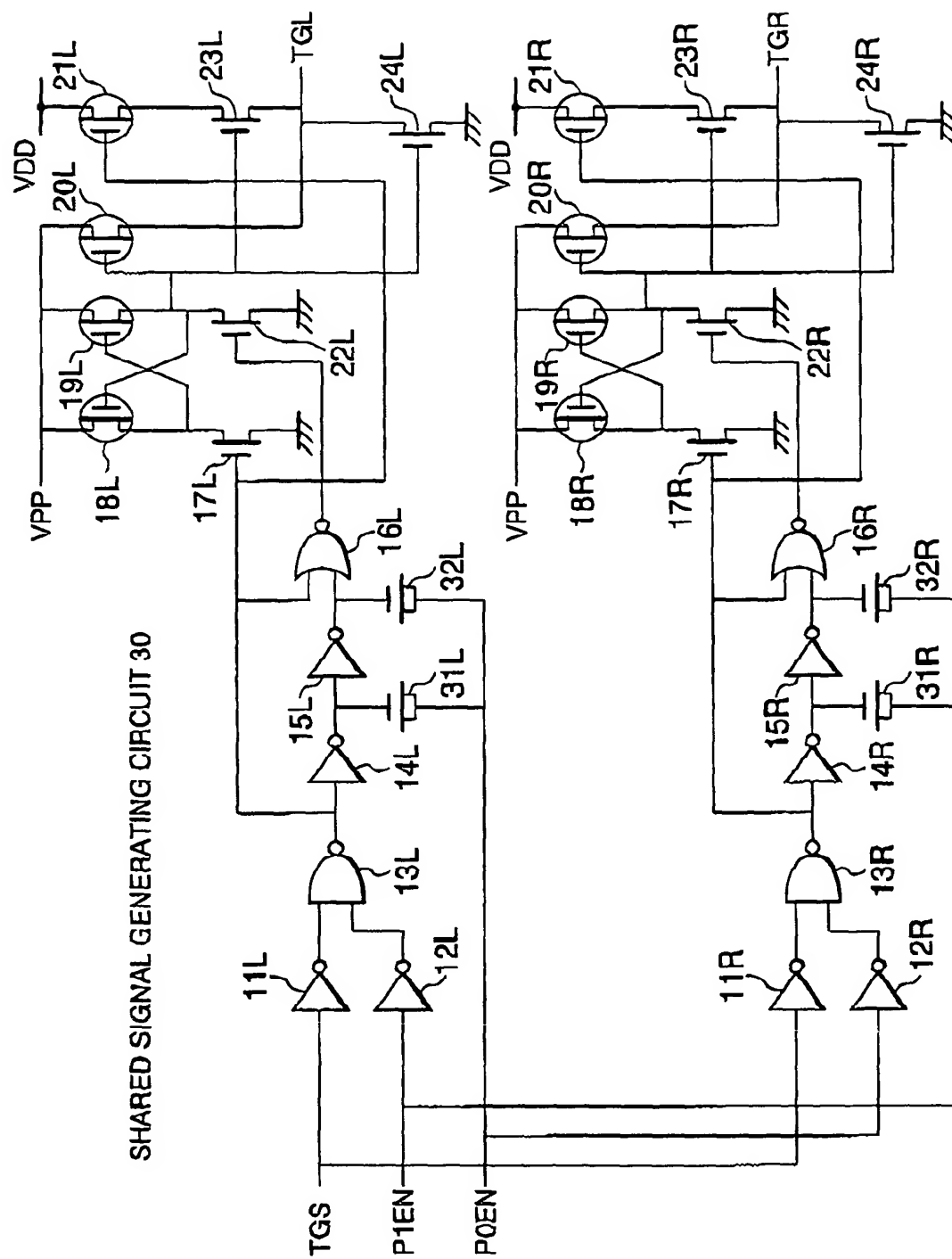


FIG. 3
(PRIOR ART)



SHARED SIGNAL GENERATING CIRCUIT 30

FIG. 4

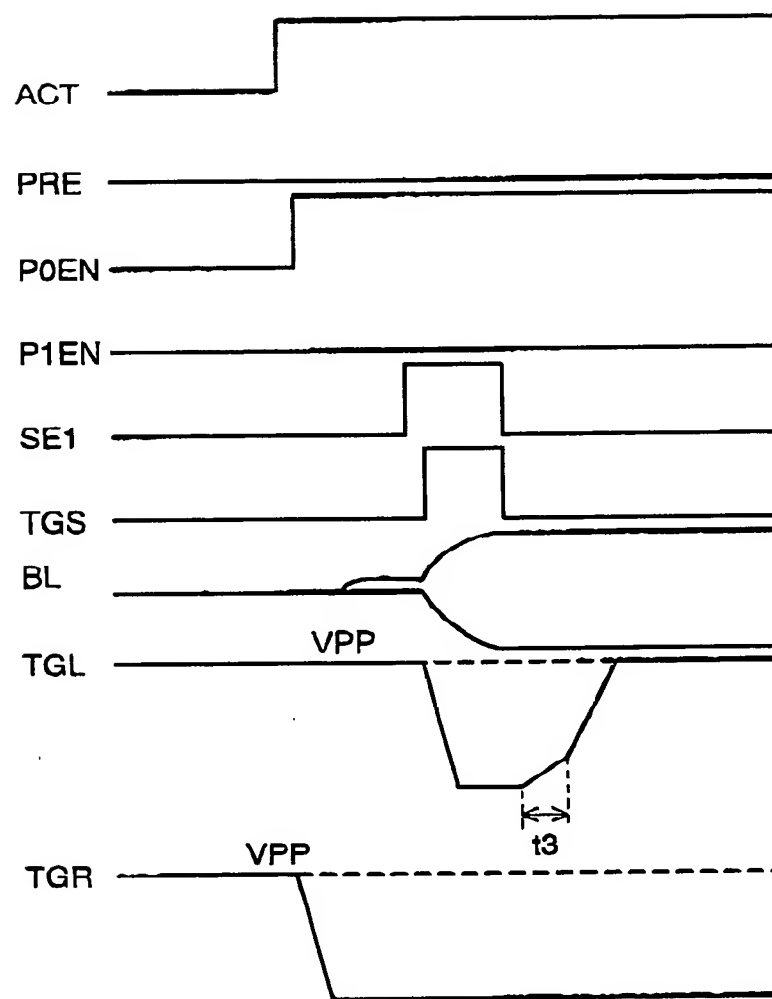


FIG. 5

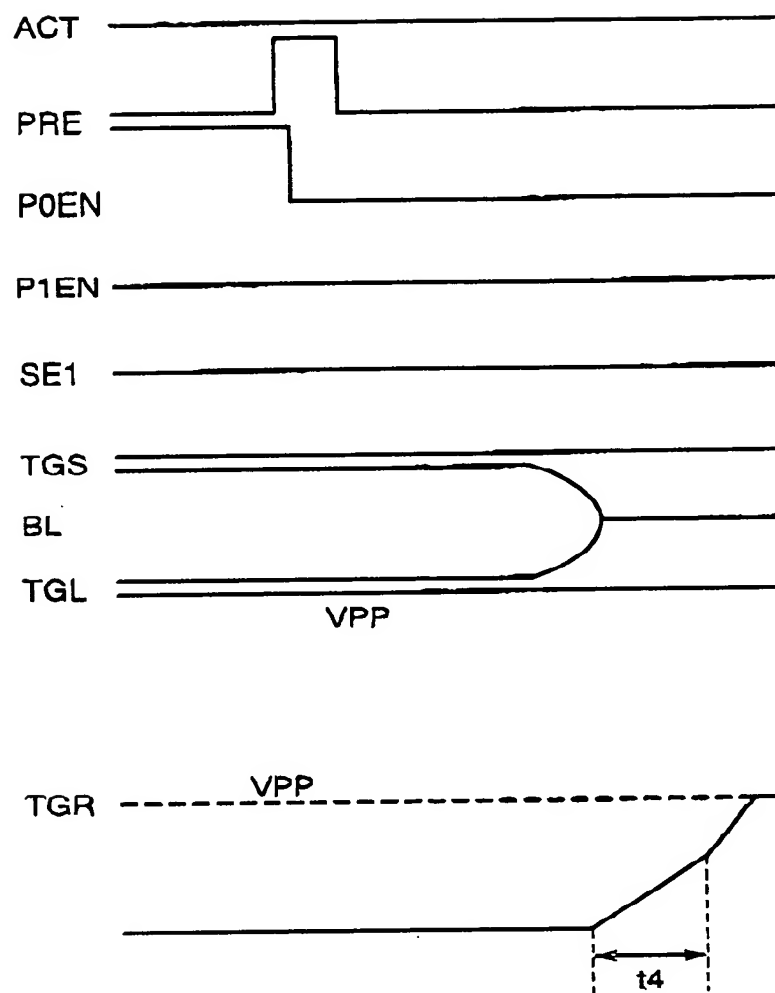


FIG. 6

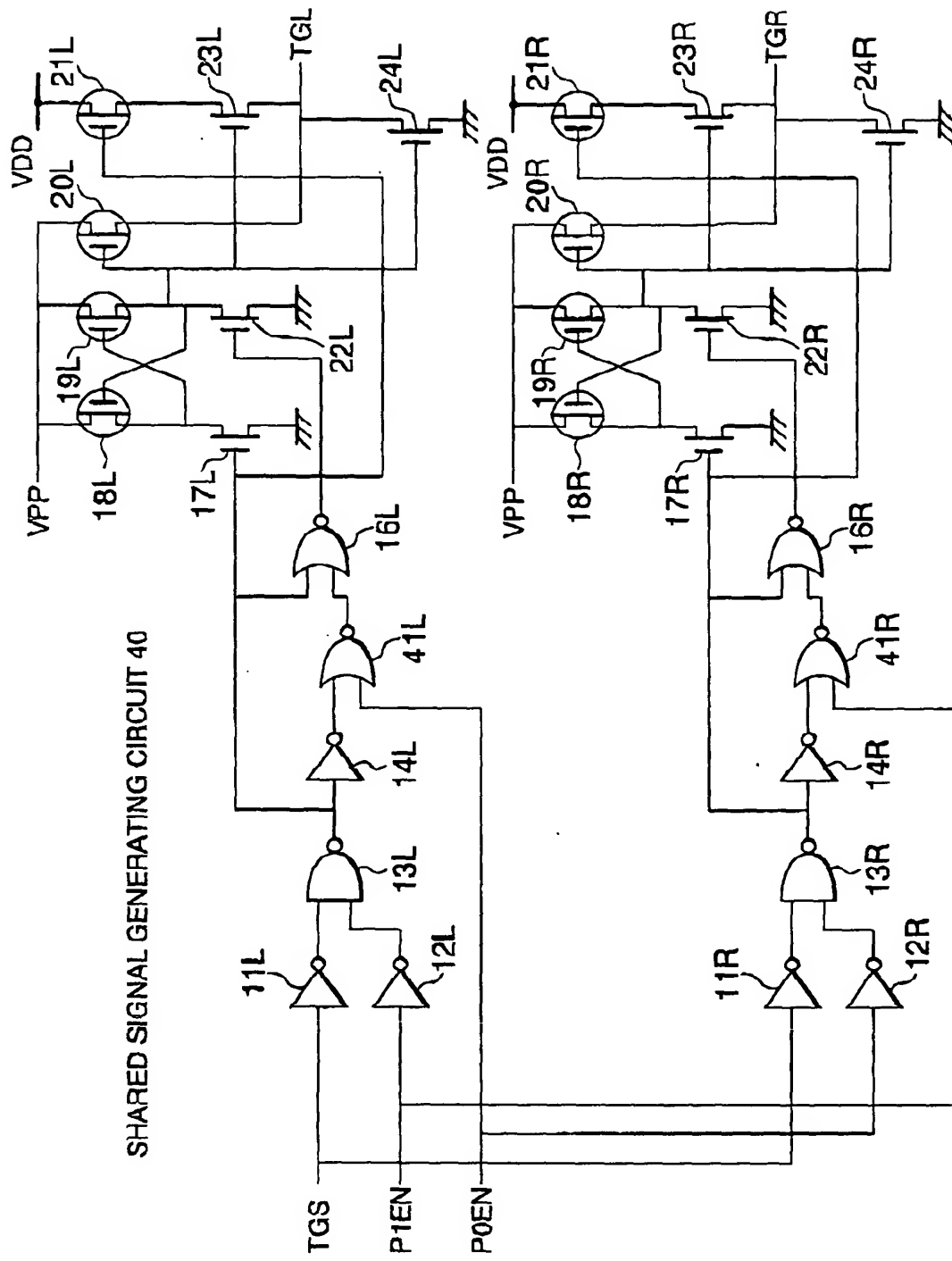


FIG. 7

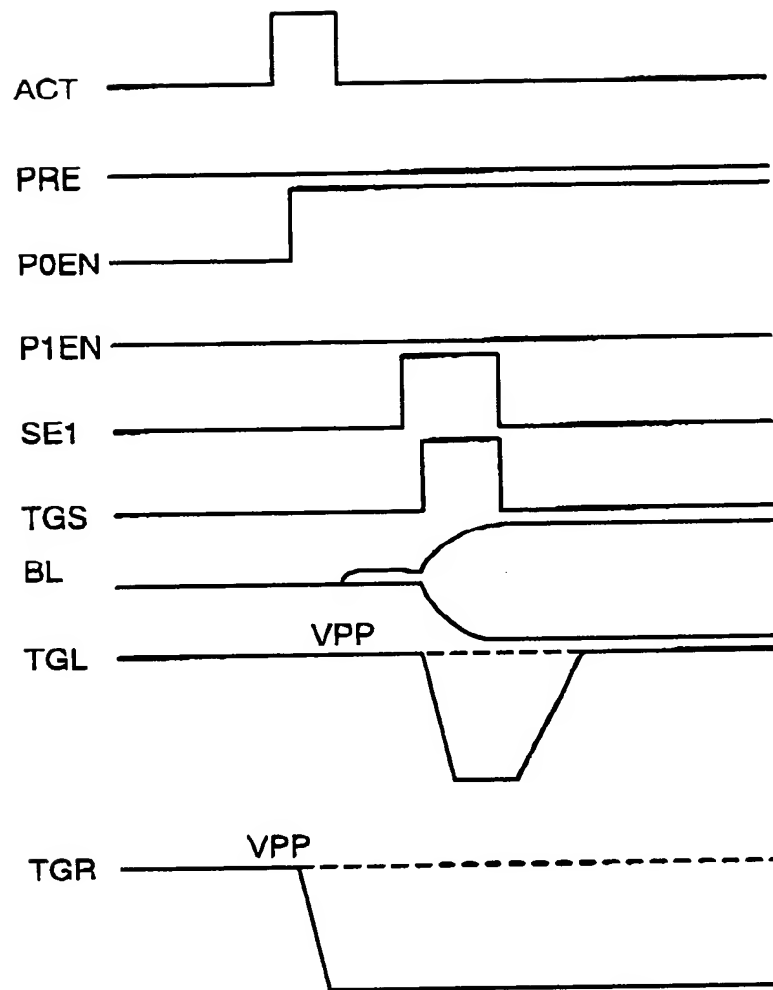


FIG. 8